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Applicant(s): Bednar et al.

Docket No.

BUR920020092US1

Application No. 10/604,277	Filing Date 7/8/2003	Examiner Tat, Binh C.	Group Art Unit 2825
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Invention: NESTED VOLTAGE ISLAND ARCHITECTURE

I hereby certify that this

Request for Reconsideration (12 pages)

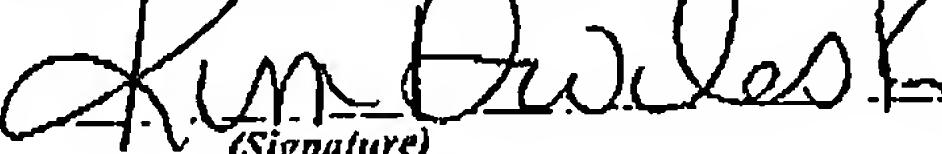
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 10/604,277

Applicant(s): Bednar et al.

Filed: July 8, 2003

Art Unit: 2825

Dkt. No.: BUR920020092US1

Examiner: TAT, BINI C.

Title: NESTED VOLTAGE ISLAND ARCHITECTURE

Honorable Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR RECONSIDERATION

This Request for Reconsideration is being filed in response to the Office Action mailed on February 14, 2006.

S/N 10/604,277

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